



IPKISS Canvas

Your graphical co-pilot for PIC design



Analyze and verify your circuits in a schematic environment.



Generate fresh ideas on an abstract level before diving into layout implementation.



Verify connectivity after layout, check device parameters, and perform back-annotations.



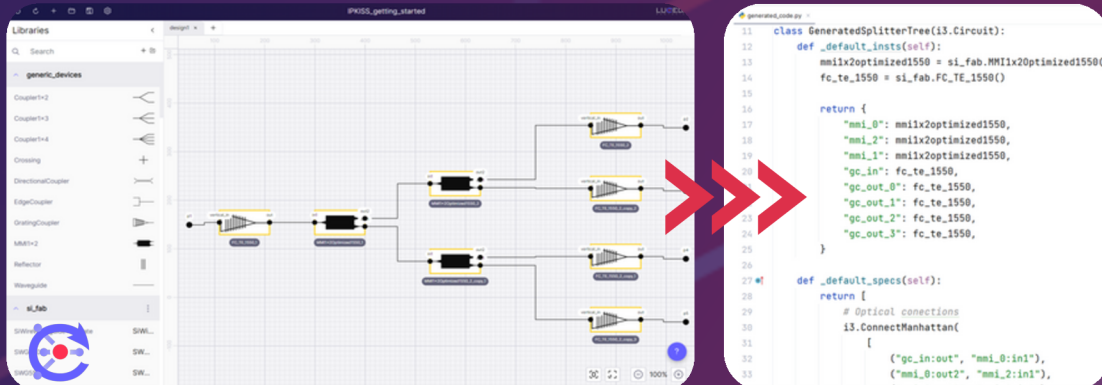
Enhance communication around your design process.



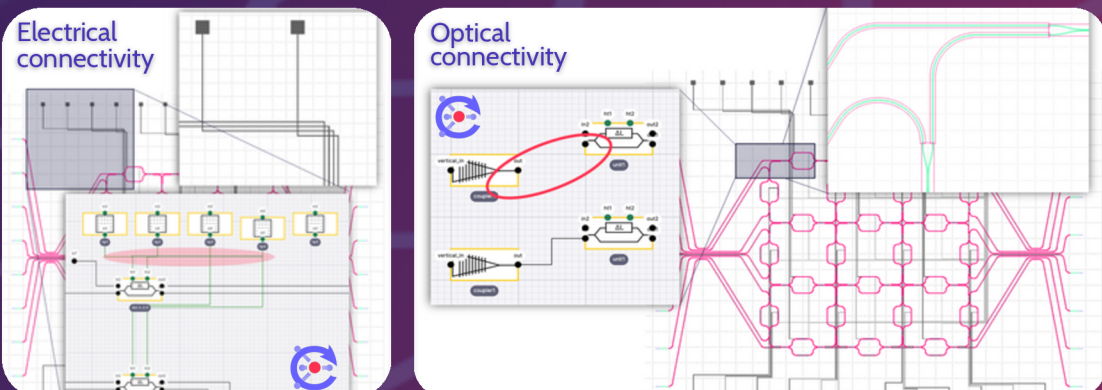
Capture schematics, drag & drop PDK components in Canvas and design your circuit easily.

TRY IT TODAY!

Schematic capture with code assistance



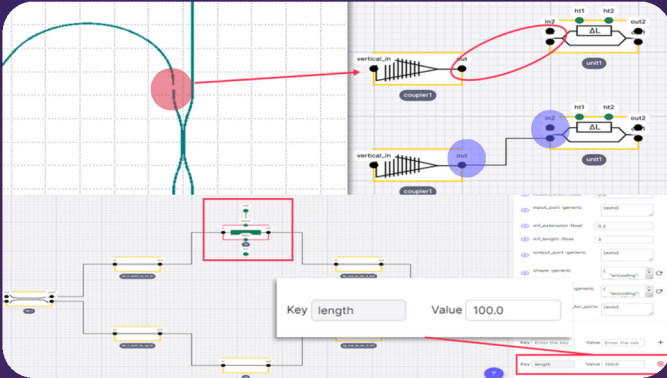
Functional verification



IPKISS Canvas brings your new and existing designs to life and detects critical design errors that are hard to spot during layout inspection!

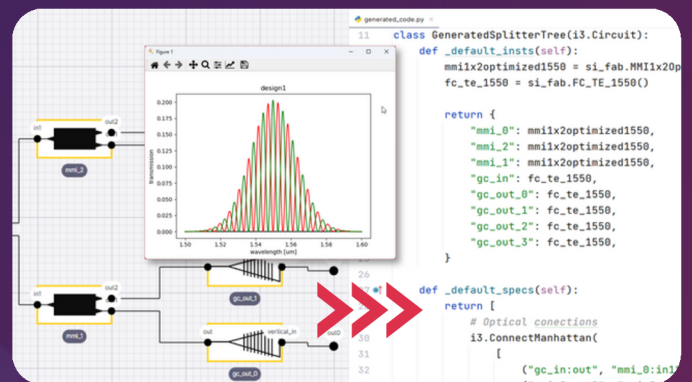


Verification



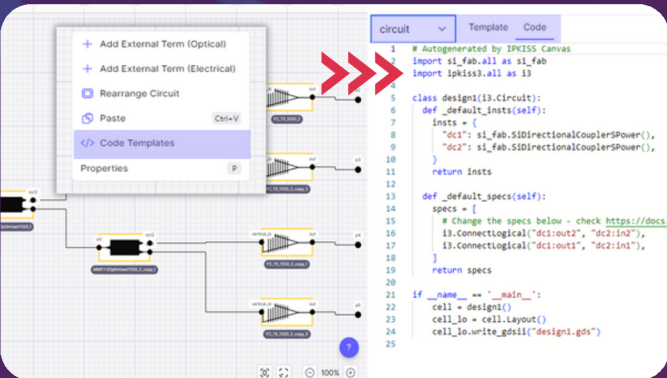
- Easily detect critical errors that go unnoticed during layout inspection.
- Before taping out, export your circuit to Canvas to functionally verify the PIC design and inspect the connectivity, parameters, and back-annotations.
- Annotate the designs you've built with essential information, extracted from the layout, such as waveguide lengths, losses, and more.

Schematic-driven Code



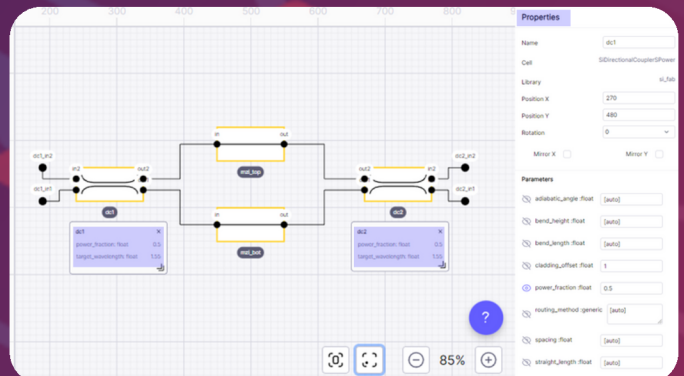
- Select one of our supported PDKs, explore the components at your disposal, capture schematics, drag & drop them onto the Canvas, and start creating your circuit.
- Once your circuit is complete in IPKISS Canvas, switch back to code to finalize the layout, inspect the circuit simulations, and prepare your PIC design for tape-out.

Easily Get Started



- Accelerate the journey into creating your first code-based PIC.
- Drag and drop PDK and library components and give shape to your ideas in a graphical environment.
- After creating your design on Canvas, use the code templates and translate it seamlessly into IPKISS code to continue your journey in code.

Effective Communication



- Share the complex design details with fellow designers, team leads, system experts, and technology specialists using the schematic Canvas environment.
- From PDK foundry components to your custom designed cells, circuit parameters, annotations, and connectivity checks - make the process of visualizing your photonic integrated circuits intuitive!